



IFW 2161

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kevin B. Coleman  
Serial No. : 09/738,439  
Filed : December 15, 2000  
Title : INTERACTIVE PRODUCT SELECTOR WITH FUZZY LOGIC ENGINE

Art Unit : 2161  
Examiner : Sana A. Al-Hashemi

Attention: Group Director  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR PERMISSION TO WITHDRAW AS ATTORNEY OF RECORD

With authorization, the undersigned attorney, on behalf of himself and the other attorneys of record, who are members or associates in the firm of FISH & RICHARDSON P.C., or who were so listed at the time of their appointment, and on behalf of any associate attorneys appointed by any of the attorneys on whose behalf this petition is filed, including the following:

Kenneth F. Kozik, Reg. No. 36,572, requests the approval of the Commissioner to withdraw as attorney(s)/agent(s) of record.

The present mailing address of the attorney(s)/agent(s) of record is:

Fish & Richardson P.C. (Boston)  
225 Franklin Street  
Boston, MA 02110

Please mail all future correspondence to the applicant as follows:

Kevin B. Coleman  
Experion Systems  
8 Clock Tower Place  
Suite 400 Maynard, MA 01754

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 31, 2005

Date of Deposit

Cassie Chandler

Signature

Cassie Chandler

Typed or Printed Name of Person Signing Certificate

The reasons for which the permission of the Commissioner is sought for withdrawal of the attorney(s)/agent(s) of record is client informed us in writing that they are discontinuing funding for all patent work and handling the matter in-house.

Notice of the attorney's request to withdraw is being sent simultaneously to the applicant and the relevant papers and files will immediately be sent the applicant upon approval of the request by the Commissioner.

At least thirty days remain for the applicant to take any action due in this case or an appropriate request for extension of time accompanies this request seeking additional time. The attorney(s)/agent(s) on whose behalf this petition is filed will ensure that the applicant has sufficient time after approval of this request to reply or employ another to reply to any outstanding requirement of the Patent and Trademark Office.

Two copies of this request are filed herewith. The Commissioner is authorized to apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

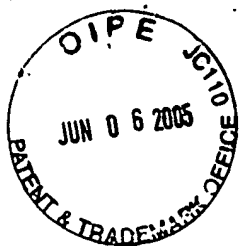
Date:

May 31, 2005



Kenneth F. Kozik  
Reg. No. 36,572

Fish & Richardson P.C.  
225 Franklin Street  
Boston, MA 02110  
Telephone: (617) 542-5070  
Facsimile: (617) 542-8906



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Lin et al.  
Application Serial No.: 10/256,401  
Filed: September 27, 2002  
For: Complementary Replacement of Material  
Patent No.: Unassigned  
Issue Date: Unassigned

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**CERTIFICATE UNDER 37 C.F.R. §3.73(b)  
ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION**

1. The assignee of the entire right, title and interest hereby seeks to take action in the PTO in this matter.

**IDENTIFICATION OF ASSIGNEE**

2. The assignee of this matter is:

**TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY**

No. 8, Li-Hsin Rd. 6  
Science-Based Industrial Park  
Hsin-Chu 300-77, Taiwan, R.O.C.

**PERSON AUTHORIZED TO SIGN**

3. Daniel R. McClure  
Attorney for Assignee

4. A chain of title from the inventor(s) to the current assignee is shown below:

a. From: Burn-Jeng Lin, Hua-Tai Lin, Ru-Gun Liu, Tsai-Sheng Gau  
and Bang-Chien Ho

To: Taiwan Semiconductor Manufacturing Company

Recorded in PTO: Reel: 013351 Frame: 0995

### **DECLARATIONS**

5. I, the undersigned, have reviewed all the documents in the chain of title of the

☒ application  
☐ patent

matter identified above and, to the best of my knowledge and belief, title is in the assignee identified above.

6. I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

7. I, the person signing below, aver that I am empowered to sign this statement on behalf of the assignee.

---

**Daniel R. McClure; Reg. No. 38,962**

Tel. No. 770-933-9500

Customer No.: 24504

**THOMAS, KAYDEN, HORSTEMEYER  
& RISLEY, L.L.P.**

100 Galleria Parkway, Suite 1750  
Atlanta, Georgia 30339-5948

Docket No. 252016-2690



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application:

Application No.:

Filed:

Title:

Commissioner for Patents  
Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST**  
**(REVOCATION OF PRIOR POWERS)**

As assignee of record of each of the patent applications listed in the table of attachment A,

**REVOCATION OF PRIOR POWERS OF ATTORNEY**

all powers of attorney previously given in each of the listed patent applications are hereby revoked, and

**NEW POWER OF ATTORNEY**

the following attorneys/agents are hereby appointed to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number.

**000047390**

Patent Trademark Office

Please direct all future correspondence and telephone calls to:

**Daniel R. McClure, Reg. No. 38,962**  
**THOMAS, KAYDEN, HORSTEMEYER & RISLEY, L.L.P.**  
100 Galleria Parkway, Suite 1750  
Atlanta, Georgia 30339  
770-933-9500

**ASSIGNEE OF ENTIRE INTEREST**


**TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY, LTD.**

8, Li-Hsin Rd. 6  
Hsinchu Science Park  
Hsinchu, Taiwan 300-77, R.O.C.

**ASSIGNEE CERTIFICATION**

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with Taiwan Semiconductor Manufacturing Company, Ltd., I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: December 27, 2004

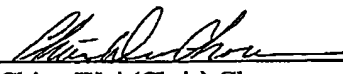
  
\_\_\_\_\_  
Chien-Wei (Chris) Chou  
Director - Intellectual Property Division

**Attachment A**

<b>No.</b>	<b>Serial No</b>	<b>TSMC No.</b>	<b>Application Title</b>	<b>Filing Date</b>	<b>Assignment (Reel/Frame)</b>
1	10/657,503	2002-0544	Phase Shift Assignments for Alternate PSM	9/8/03	014479/0625
2	10/425,322	2002-0382	Quartz Damage Repair Method for High-End Mask	4/29/03	014025/0791
3	10/661,746	2002-0353	Method to Shrink Cell Size in a Split Gate Flash	9/12/03	014500/0769
4	10/796,891	2002-0254B	Storage Element and SRAM Cell Structures Using Vertical FETS Controlled by Adjacent Junction Bias Through Shallow Trench Isolation	3/9/04	Recorded 014006/0344 at the parent application USP6,759,699
5	10/689,431	2002-0218	Method for Measuring Capacitance-Voltage Curves for Transistors	10/20/03	014622/0410
6	10/714,998	2002-0212	Water Soluble Negative Tone Photoresist	11/17/03	014712/0284
7	10/796,430	2002-0189	Placement and Routing Method to Reduce Joule Heating	3/9/04	015069/0548
8	10/307,646	2002-0132	Scheme for Eliminating the Channel Unexpected Turn-On During ESD Zapping	12/2/02	013547/0654
9	10/388,274	2002-0127	Light Guide for Image Sensor	3/12/03	013876/0368
10	10/256,401	2002-0064	Complementary Replacement of Material	9/27/02	013351/0995
11	10/788,175	2002-0047	Method for Making Improved Bottom Electrodes for Metal-Insulator-Metal Crown Capacitors	2/26/04	015033/0280
12	10/723,237	2001-1630	Method to Form a Robust TiCl <sub>4</sub> Based CVD TiN Film	11/26/03	
13	10/755,500	2001-1596	Single Trench Repair Method with Etched Quartz for Attenuated Phase Shifting Mask	1/12/04	014896/0968
14	10/205,790	2001-1582	Full Sized Scattering Bar Alt-PSM Technique for IC Manufacturing in Sub-Resolution ERA	7/26/02	013148/0435

15	10/781,182	2001-1582D	Full Sized Scattering Bar Alt-PSM Technique for IC Manufacturing in Sub-Resolution ERA	2/18/04	Recorded 013148/0435 at the parent application USP6,711,732
16	10/781,176	2001-1582B	Full Sized Scattering Bar Alt-PSM Technique for IC Manufacturing in Sub-Resolution ERA	2/18/04	Recorded 013148/0435 at the parent application USP6,711,732
17	10/782,670	2001-1582E	Full Sized Scattering Bar Alt-PSM Technique for IC Manufacturing in Sub-Resolution ERA	2/19/04	Recorded 013148/0435 at the parent application USP6,711,732
18	10/680,577	2001-1701B	SRAM Cell Design for Soft Error Rate Immunity	10/7/03	Recorded 013408/0285 at the parent application USP6,649,456
19	10/682,269	2002-0026B	Asymmetrical Reset Transistor with Double-Diffused Source for CMOS Image Sensor	10/9/03	Recorded 013422/0370 at the parent application USP6,642,076

Date: December 27, 2004

  
 Chien-Wei (Chris) Chou  
 Director - Intellectual Property Division